REMARKS

Claims 1-17 and 20-27 are pending, with claims 1, 2, 4, 5, 15-17, 20, and 22 amended and claims 26 and 27 added by the present amendment.

In the Official Action, the Notice of Allowance of previously pending claims 1-17 and 20-25 was withdrawn; claims 1-1 15, 17 and 20-25 were rejected under 35 U.S.C. 103(a) as being unpatentable over Kim (U.S. Patent 6,069,619, hereinafter Kim) in view of Tani et al. (U.S. Patent 6,011,880, hereinafter Tani); claim 16 was rejected under 35 U.S.C. 103(a) as being unpatentable over Kim and Tani in view of Kudo et al (U.S. Patent 6,658,583, hereinafter Kudo); and claims 12-14 were indicated as containing allowable subject matter.

Applicant acknowledges with appreciation the indication of allowable subject matter.

Apparatus claims 1, 2, 5, 15, and 16 are amended to avoid an interpretation under 35 U.S.C. 112, sixth paragraph. Claims 4 and 17 are amended to correct informalities therein. Claims 1, 2, 15, 20, and 22 are amended to recite features related to determining a storage section for an image signal by using a storage related signal. Support for this amendment is found at least in Applicant's originally filed Figures 4-6. New claims 26 and 27 are directed to additional features disclosed in Applicant's originally filed specification. No new matter is added.

Briefly recapitulating, amended claim 1 is directed to:

> An apparatus for processing displayed data in a system having a computer for processing data and a display device with an amplifier for amplifying input signals from the computer, the apparatus comprising:
>
> a memory having plural storage sections;
>
> a Micom configured to control the display device, and to output a selection signal and a vertical synchronizing signal; and
>
> a comparator configured to compare the selection signal with the vertical synchronizing signal, and to output a storage related signal to the micom, and

wherein the micom is configured to use the storage related signal to determine a storage section in the memory for storing an image signal corresponding to the selection signal.

Amended independent claim 2 recites, *inter alia*, a Micom configured to control the display, to output a selection signal and to generate a vertical synchronizing signal, and to output a storage related signal corresponding to a comparison of the selection signal and the vertical synchronizing signal; and a memory configured to save an image signal corresponding to the selection signal in accordance with the storage related signal.

Amended independent claim 15 recites, *inter alia*, a comparator configured to compare the selection signal with the vertical synchronizing signal and to output a storage related signal to the micom, and wherein the micom is configured to use the storage related signal to determine a storage section in the memory for storing an image signal corresponding to the selection signal;

Amended independent claim 20 recites, *inter alia*, comparing the storage command signal and a vertical synchronizing signal to generate a selection signal that identifies a storage section in a memory to store an image signal corresponding to the storage command signal; and storing the image signal in the storage section in accordance with the selection signal.

Amended independent claim 22 recites, *inter alia*, comparing the storage command signal and a vertical synchronizing signal to generate a storage section selection signal; storing an image signal displayed on the display device in a storage section of a memory corresponding to the storage section selection signal.

New independent claim 26 recites, *inter alia*, a comparator configured to compare the selection signal with the vertical synchronizing signal and, if logic levels of the selection signal and the vertical synchronizing signal are equal, output a first command signal identifying a storage section in the memory, and wherein the Micom is arranged to save an image signal corresponding the selection signal in the storage section in response to the first command signal,

and to end storage of the image signal in response to a second command signal outputted from the comparator.

New independent claim 27 recites, *inter alia*, a comparator configured to compare the selection signal with the vertical synchronizing signal and output a storage related signal to the micom identifying a storage section for storing an image signal corresponding to the selection signal.

Kim describes an apparatus and method for displaying the status of a current displayed power management signaling (DPMS) mode in a display device that uses an on screen display (OSD) circuit. In Kim, the DPMS mode is overridden for a predetermined time upon an attempt to operate any front panel key. A message indicating the DPMS state is displayed through an OSD circuit.

In particular, Kim describes a method for 1) determining the current DPMS mode based on an input status of video sync signals by, 2) determining whether a horizontal sync signal input is present, 3) determining whether a vertical sync signal input is present when the horizontal sync signal input is present; and, 4) determining whether the vertical sync signal input is present when the horizontal sync signal input is not present, 5) storing, in the internal memory of the microcomputer, status data corresponding to the determined current DPMS mode; and 6) driving the OSD circuit for a predetermined time using an OSD drive signal output from the microcomputer, to display on the display device a message indicative of the stored status data.

However, as acknowledged in the Official Action, Kim does not disclose or suggest Applicant's claimed comparator or step of comparing. To cure this deficiency, the Official Action applies Tani.

Tani describes an encoded signal reading device capable of reading a decoding a tessellated symbol. Tani describes an encoded symbol reading device that can accurately decode a two-dimensional symbol, and which has a simple construction. In particular, Tani describes

that an 8 bit digital image signal is transmitted from the A/D converter to a comparator, and at the same time, threshold data is transmitted to the comparator. Tani further describes that an image data (digital) and the threshold data is compared on a pixel basis, and if the image data is equal to or greater than the threshold data, a HIGH signal is outputted from the comparator 10, and if the image data is less than the threshold data, a LOW signal is outputted.

However, Kim and Tani each fail to disclose or suggest selecting a storage section for storing a display signal (or an image signal) based on a vertical sync signal, let alone based upon a comparison of a vertical sync signal with a selection signal. Indeed, Tani does not disclose or suggest a comparator that compares a selection signal (or a selection signal by user) with the vertical synchronizing signal. Tani also does not disclose or suggest selecting a storage section for storing a display signal (or image signal) based on the 8bit digital image signal or the threshold data used in Tani's comparator.

As none of the cited art, individually or in combination, discloses or suggests at least the above-noted features of independent claims 1, 2, 15, 20, 22, 26 and 27, Applicant submits the inventions defined by claims 1, 2, 15, 20, 22, 26 and 27, and all claims depending therefrom, are not rendered obvious by the asserted references for at least the reasons stated above.[1]

[1] MPEP § 2142 "...the prior art reference (or references when combined) must teach or suggest **all** the claim limitations.

CONCLUSION

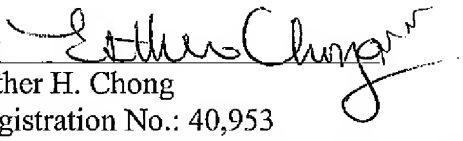
In view of the above amendments and remarks, Applicant believes the pending application is in condition for allowance.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Michael Monaco, Registration No. 52,041, at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37.C.F.R. §§1.16 or 1.14; particularly, extension of time fees.

Dated: January 28, 2008

Respectfully submitted,

By 

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